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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/887,504	06/22/2001	Douglas Deao	TI-30100	7710
7590	11/29/2004		EXAMINER	
Robert L. Troike Texas Instruments Incorporated P.O. Box 655474, MS 3999 Dallas, TX 75265			CHAVIS, JOHN Q	
			ART UNIT	PAPER NUMBER
			2124	

DATE MAILED: 11/29/2004

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary	Application No.	Applicant(s)	
	09/887,504	DEAO ET AL.	
	Examiner	Art Unit	
	John Chavis	2124	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

1) Responsive to communication(s) filed on 30 July 2004.

2a) This action is **FINAL**. 2b) This action is non-final.

3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

4) Claim(s) 1-8 is/are pending in the application.

4a) Of the above claim(s) _____ is/are withdrawn from consideration.

5) Claim(s) _____ is/are allowed.

6) Claim(s) 1-8 is/are rejected.

7) Claim(s) _____ is/are objected to.

8) Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

9) The specification is objected to by the Examiner.

10) The drawing(s) filed on _____ is/are: a) accepted or b) objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).

11) The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).

a) All b) Some * c) None of:

1. Certified copies of the priority documents have been received.
2. Certified copies of the priority documents have been received in Application No. _____.
3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

1) Notice of References Cited (PTO-892)

2) Notice of Draftsperson's Patent Drawing Review (PTO-948)

3) Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
Paper No(s)/Mail Date _____

4) Interview Summary (PTO-413)
Paper No(s)/Mail Date _____

5) Notice of Informal Patent Application (PTO-152)

6) Other: _____

Response to Arguments

1. Applicant's arguments filed 07/30/04 have been fully considered but they are not persuasive. The rejection previously cited in view of claim 1 is still considered pertinent to the applicant's invention. The applicant claims that his system provides for emulation by transferring data between a host processor and a target processor; while Proteat provides for DLL calls within a single processing system. The applicant further admits on the bottom of page 7 of the present response that there is a target procedure in a target DLL; but, he indicates that ther is not target processor or loading emulation software support for a new target processor. However, the applicant is hereby reminded that a target DLL inherently implies that the DLL is to be utilized in a different system (processor) Furthermore, in col. 2 line 65-col. 3 line 3, Proteat indicates that the various items are tangibly embodied in the memory 14 (local) and/or one or more data storage devices 20-22 (which may or may not be local) and/or received from one or more data communication devices coupled to the computer (i.e. transferred from a target processor). Therefore, the feature is considered provided for in Proteat's system.

The applicant's reference to claims 2-8 has been discussed in the previous action. The applicant indicates that the various item are known terms; which, provides further support for the 35 U.S.C. 103 Rejections previously presented. The remainder of the remarks references the features discussed above (multiple processors). Therefore, the previous rejection is considered proper and is hereby sustained.

2. Any inquiry concerning this communication or earlier communications from the examiner should be directed to John Chavis whose telephone number is (703) 571-3720. The examiner can normally be reached on M-Tue & Th-F, 8:30-5:00.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Kakali Chaki can be reached on (703) 571-3719. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

JC
November 23, 2004



JOHN CHAVIS
PATENT EXAMINER
ART UNIT 2124